Google scholar | "description file" and layout and "description lar | Search |

Scholar | Articles and patents | | anytime | | include citations | ☒ Create email alert Results 1 - 100 of about 721. (0.45 sec)

GraphXML—An XML-based graph description format

psu.edu [PDF]

I Herman, M Marshall - Graph Drawing, 2001 - Springer
... The result is GraphXML, a graph **description language** based on XML. ... For example, the <graph>
tag can use keys such as version, vendor, preferred- **Layout**, isPlanar, isDirected ... dataref> tag):
data that is referenced externally rather than embedded in the graph **description file**. ...
Cited by 46 - Related articles - BL Direct - All 12 versions

[PDF] An integrated automated **layout** generation system for DSP circuits

berkeley.edu [PDF]

JM Rabaey, SP Pope, RW ... - IEEE Transactions on ..., 1985 - bwrc.eecs.berkeley.edu
... An Integrated Automated **Layout** Generation System for DSP Circuits JAN M. RABAEY, member,
ieee, STEPHEN P. POPE, ROBERT W. BRODERSEN, fellow, ieee Abstract—ha integrated CAD
system for the automated design of digital signal-processing (DSP) circuits for aodio ...
Cited by 42 - Related articles - All 2 versions

VLSI CAD tool integration using the Ulysses environment

ML Bushnell, SW Director - Proceedings of the 23rd ACM/IEEE ..., 1986 - portal.acm.org
... cad-tool bboard file: **layout** *values* file-name *description* I CIF language **description file** for
an IC ... Since the input for the **layout** synthesizer comes from several other programs, it is ... For CAD
tools that automatically generate **layouts**, we want Ulysses to help the designer create ...
Cited by 42 - Related articles - All 4 versions

A real-time system **description language**

psu.edu [PDF]

D Niehaus, JA Stankovic, K Ramamritham - rtas, 1995 - computer.org
... Figure 1: System **Description Language** Top Level ... The system **layout** and network topology
specifications are exceptions since they are specified only once as inde- pendent ... processes each
of the Spring-C and SDL source files, forming a cumulative system **description file** (SDF ...
Cited by 20 - Related articles - All 9 versions

3D integrated circuit **layout** visualization using VRML

LS Indrusiak, RA da Luz Reis - Future Generation Computer Systems, 2001 - Elsevier
... The 2D **layout description file** is done using the widely used CIF format familiar to
every integrated circuit **layout** designer. ... 6. Conclusions. The use of VRML as an
integrated circuit **layout description language** was presented. ...
Cited by 17 - Related articles - BL Direct - All 4 versions

[PDF] An approach for automatic data virtualization

ohio-state.edu [PDF]

L Weng, G Agrawal, U Catalyurek, T Kurc, S ... - system - cse.ohio-state.edu
... First,our tool can correctlyand efficiently handle a varietyofdifferent data **layouts**. Second,our
system scaleswell as the number ofnodes or the amount of data is scaled. ... Pipelined data chunks
STORM **description file** Dataset **layout description file** Dataset storage P3 P2 P1 ...
Cited by 24 - Related articles - View as HTML - All 9 versions

[PDF] Computer-aided design of fuzzy systems based on generic VHDL specifications

psu.edu [PDF]

T Hollstein, SK Halgamuge, M Glesner - IEEE Transactions on Fuzzy ..., 1996 - Citeseer
... The decision for VHDL as **description language** has been made, since a lot of design experience
with VHDL specifications and the SYNOPSYS simulation and synthesis toolkits were already ...
Physical **Layout Description File** (ASIC) or Device Configuration Bitstream (FPGA) ...
Cited by 46 - Related articles - View as HTML - BL Direct - All 8 versions

Bridging the paper and electronic worlds: the paper user interface

W Johnson, H Jellinek, L Klotz Jr, R Rao, ... - Proceedings of the ..., 1993 - portal.acm.org
... of digital data on paper goes beyond that of simple character and **layout** recognition, for ... a
WYSIWYG form editor, a form dwxip- tion language, an action **description language**, and a ... identity
of the form, locates the appropriate action **description file**, and processes the page image ...
Cited by 119 - Related articles - All 3 versions

[PDF] ORTEP-3 for Windows

gla.ac.uk [PDF]

LJ Farrugia - J. Appl. Crystallogr, 1997 - chem.gla.ac.uk
... 1.1 NEW FEATURES IN VERSION 2 The creation of Version 2 has involved a complete
rewrite of the GUI code. The GUI retains virtually all the useful features of Version 1,
and the **layout** has remained very similar. New features are : ...
Cited by 14466 - Related articles - View as HTML - All 7 versions

Embarc: an efficient memory bank assignment algorithm for retargetable compilers

psu.edu [PDF]

JD Hiser, JW Davidson - Proceedings of the 2004 ACM SIGPLAN/ ..., 2004 - portal.acm.org
... Partition **description file** ... Lastly, the compiler provides informa- tion about the stack **layout** which
is used by the simulator to bring local variables into and out ... 2.3 Partition **Description Language**
One input to EMBARC is a description of the memory hierarchy on the target machine. ...
Cited by 22 - Related articles - BL Direct - All 8 versions

Automatic generation of FPGA routing architectures from high-level descriptions

psu.edu [PDF]

V Betz, J Rose - Proceedings of the 2000 ACM/SIGDA eighth ..., 2000 - portal.acm.org
... routed nets and path-based timing-analysis, one must specify various timing parameters in the
architecture **description file**. ... ing the start points of the segments in this way makes it easier to **lay
out** the FPGA ... A tile-based FPGA **layout** is one in which only a single logic block and its ...
Cited by 33 - Related articles - All 16 versions

[CITATION] A high level synthesis tool for MOS chip design

J Dussault, CC Liaw, MM Tong - 21st Conference on Design Automation, 1984, 1984

A novel analog module generator environment

psu.edu [PDF]

M Wolf, U Kleine, BJ Hosticka - ... of the 1996 European conference on ..., 1996 - portal.acm.org

... **layouts**. The design rules are stored in a technology **description file**. The ... devices.
Thus a modular **layout** style results naturally. To support an automatic **lay- out** tool,
a library with parameterizable modules is neces- sary. The ...

[CITATION] Hierarchical circuit extraction with detailed parasitic capacitance

GM Tarolli, WJ Herman - 20th Conference on Design Automation, 1983, 1983

A generic tool set for application specific processor architectures

psu.edu [PDF]

F Engel, J Nührenberg, GP Fettweis - Proceedings of the eighth ..., 2000 - portal.acm.org

... SFB358/TP6. compilers or physical **layout** generators are available. ... 2. Architecture dependent:
This part is derived from the Processor **Description File** (PD-File). ... LISA - Machine **Description
Language** for Cycle-Accurate Models of Programmable DSP Architectures. ...

[CITATION] A new analytical approach to estimate the effects of SEUs in TMR architectures implemented through SRAM-based FPGAs

psu.edu [PDF]

L Sterpone, M Violante - IEEE Transactions on Nuclear Science, 2005

[PDF] CODE: A development environment for OWL-S web services

cmu.edu [PDF]

N Srinivasan, M Paolucci, K ... - Demo paper in 3rd ..., 2004 - www-cgi.cs.cmu.edu

... OWL-S is a Web Services **description language** that enriches Web Services descriptions based
on ... form editor and the text editor are arranged in a tabular **layout** stacked one ... description the
following two files are required: a process model **description file** containing information ...

[CITATION] A vertically integrated VLSI design environment

J Rosenberg, D Boyer, J Dallen, S Daniel, C Poirier, J ... - 20th Conference on Design ..., 1983

m-links: An infrastructure for very small internet devices

psu.edu [PDF]

BN Schilit, J Trevor, DM Hilbert, TK ... - Proceedings of the 7th ..., 2001 - portal.acm.org

... 1.1 Experience With a Web Transducer The Digestor [5] is an intelligent proxy that performs
semantic compression and **layout** modification of web pages for a PDA or laptop. ... Navigation
links are identified using a number of page **layout** heuristics. ...

GEMS: an automatic **layout** tool for MIMOLA schematics

VV Venkataraman, CD Wilcox - Proceedings of the 23rd ACM/IEEE ..., 1986 - portal.acm.org
GEMS: AN AUTOMATIC **LAYOUT** TOOL FOR MIMOLA SCHEMATICS ... for placement of schematics
in an esthetically pleasing form are different from the constraints for placement aimed toward
minimum area **layouts**. ... The structure **description file** is analyzed for syntax errors. ...
Cited by 7 - Related articles - All 4 versions

[CITATION] An overview of the COBRA-ABS high level synthesis system formulti-FPGA systems
AA Duncan, DC Hendry, P Gray - IEEE Symposium on FPGAs for Custom ..., 1998
Cited by 39 - Related articles - All 4 versions

PAPS—The parallel program performance prediction toolset
H Wabnig, G Haring - Computer Performance Evaluation Modelling ..., 1994 - Springer
... Such routing schemes are almost always regularly structured and hence the routing **description
file** can be ... The viewer automatically generates a "good" **lay- out**, which can be adjusted by
command line ... The **layout** can also be edited manually by using the mouse to move nodes ...
Cited by 36 - Related articles - All 4 versions

Hardcopy lossless data storage and communications for electronic document processing systems
F Zdybel Jr, HW Sang Jr, JO Pedersen, ZE ... - US Patent ..., 1996 - Google Patents
... c i > t: 4-1 U OI 5~- XH , a ^ "*3 aj to- \J) og 4= = m « a 01 > 01 ^4 sj E N L I i J 1 • [* • a; x 1 i
Page-**Description Language** .. ... _._ j 1 (! J _,s r\ 1 1— 1 C |£l- ^ Software adding encoding-glyphs
to document **description file** se-' uoiiejduaB juau/noo o I n <j 1 Language composing 0) ng ...
Cited by 31 - Related articles - All 2 versions

A timing verification system based on extracted MOS/VLSI circuit parameters
P Ng, W Glauert, R Kirk - Proceedings of the 18th Design ..., 1981 - portal.acm.org
... system as shown in Figure 3. The logic description is entered in BOLT logic **description language**
EI3. ... The mask design data may be in digitized form, or in symbolic **layout** form ... It is essential
however that the BOLT logic **description file** is available and that the nodal capacitances ...
Cited by 12 - Related articles - All 2 versions

Automated generation of megacells in an integrated circuit design system
OS Bair, MR Carbonara - US Patent 5,898,595, 1999 - Google Patents
... Other forms of logic design 103 entry include VHDL (VHSIC Hardware **Description Language**)
20 descriptions ... to enable less skilled integrated circuit designers to produce finished **layouts** while
focusing ... on the placement and routing required to produce the finished **layout** 111. ...
Cited by 13 - Related articles - All 3 versions

Mobyle: a new full web bioinformatics framework   nih.gov [HTML]
B Neron, H Menager, C Maufrais, N Joly, J ... - ..., 2009 - Oxford Univ Press
... can be overridden to generate program-specific forms and job result pages with particular **layouts**. ...
2). This functionality relies on via specific **layout** tags or even HTML snippets ... also integrates a
perl library to generate the corresponding Mobyle XML **description file** from program ...
Cited by 12 - Related articles - All 12 versions

[PDF] LSI design verification using topology extraction

lscheffer.com [PDF]

L Scheffer, R Apte - 12th Asilomar Conference on Circuits Systems ..., 1978 - lscheffer.com

... level functionality and logic level functionality of the mask **layout**. The-topology extraction system
is composed of two phases. ... After all the logic gates are extracted, the **description file** is generated. ...
A **description language** is being developed for logic gate structures. ...

Cited by 14 - Related articles - View as HTML - All 3 versions

Multithreaded, mixed hardware description languages logic simulation on engineering workstations

T Chan - US Patent 6,466,898, 2002 - Google Patents

Page 1. US006466898B1 (12) United States Patent Chan (io) Patent No.: US
6,466,898 BI (45) Date of Patent: Oct. 15,2002 (54) MULTITHREADED, MIXED
HARDWARE DESCRIPTION LANGUAGES LOGIC SIMULATION ON ...

Cited by 18 - Related articles

[PDF] simBio: a Java package for the development of detailed cell models

physiome.org.nz [PDF]

Ã Nobuaki Sarai, S Matsuoka, A ... - Progress in biophysics and ..., 2006 - physiome.org.nz

... Biological model **description language** (4) CellML is a computer-readable format to store and ...
metabolic pathways according to user-defined guidelines and multiple **layout** strategies. ... For
example, the XML **description file**, which corresponds to the action potential generation ...

Cited by 25 - Related articles - View as HTML - All 5 versions

[PDF] A platform-independent user interface **description language**

psu.edu [PDF]

H Stottner - 2001 - Citeseer

... Although ISL doesn't currently support features controlling the **layout** of interfaces its syntax is
restricted to the essential. ... Page 13. Platform-Independent UI **Description Language** Existing
Solutions ... The interpretation of an UIML-Interface **description file** displays a user interface. ...

Cited by 5 - Related articles - View as HTML - All 3 versions

Object-action user interface management system

MH Notess, SJ Warren, T Heiserman - US Patent 5,438,659, 1995 - Google Patents

... methods; (3) positioning and labeling of common elements; (4) relative screen **layout**; (5) error ...
An advantage of this **description file** interpreter is the resulting consis- tency among the ... and display
technology through the use of a high-level **description language**, the interpreter ...

Cited by 22 - Related articles

A HW/SW design methodology for embedded SIMD vector signal processors

psu.edu [PDF]

JP Robelly, G Cichon, H Ahlendorf, G ... - International Journal of ..., 2005 - Inderscience

... (1999), the purpose of RNA is not to become a new generic Architecture **Description Language**
(ADL). ... In Figure 4 we can observe the processing stages we are applying to the machine
description file. At first, an instruction set architecture **layout** is created. ...

Cited by 14 - Related articles - All 18 versions

[PDF] Design of transport triggered architecture processor for discrete cosine transform

psu.edu [PDF]

J Heikkinen, J Sertamo, T Rautiainen, J ... - 15th Annual IEEE ..., 2002 - Citeseer
... buses, FUs and RFs and their connectivity, are described in an architec- ture **description file**. ...
constraints, the difference is about 5% compared to final post **layout** results. ... of application-specific
instruction-set processors (ASIPs) using a machine **description language**," IEEE Trans ...
Cited by 9 - Related articles - View as HTML - All 8 versions

A unified header compression framework for low-bandwidth links

J Lilley, J Yang, H Balakrishnan, S ... - Proceedings of the 6th ..., 2000 - portal.acm.org
... Figure 2: **Layout** of a typical compressed header, including Context ID, bit-field for the ... Since the
header **description language** was described in Section 3, the latter two parts occupy this ... that the
tools use to translate the header information found in the **description file** to protocol ...
Cited by 29 - Related articles - All 30 versions

Test case management tools for accessibility testing

S Herramhof, H Petrie, C Strobbe, E ... - ... Helping People with ..., 2006 - Springer
... The Test Case **Description Language** TCDL [2] developed within the scope of BenToWeb, not
only states whether a test ... 1. Status information about a test case **description file** ... Open questions
need information about **layout** and spacing to be presented correctly to user panel by ...
Cited by 8 - Related articles - BL Direct - All 7 versions

Method and system for creating messages including image information

NL Mayle, DL Rose... - US Patent 6,018,774, 2000 - Google Patents
Page 1. United States Patent Mayle et al. US006018774A [ii] Patent Number: [45] Date
of Patent: 6,018,774 Jan. 25, 2000 [54] METHOD AND SYSTEM FOR CREATING
MESSAGES INCLUDING IMAGE INFORMATION [75] Inventors ...
Cited by 13 - Related articles - All 2 versions

[CITATION] Configuration and management of a real-time smart transducer network

S Pitzek, W Elmenreich - IEEE Conference Emerging Technologies and Factory ..., 2003
Cited by 16 - Related articles - All 8 versions

triple-s XML: A Standard Within a Standard

K Hughes, S Jenkins, G Wright - Social Science Computer ..., 2000 - ssc.sagepub.com
... A text-based **layout** avoids the problems associated with transferring binary files, which is clearly
an important ... Or, in the use we put it to here, as a survey **description language**. ... the same name
but with the extension "ASC." The syntax of a triple-s XML **description file** accords with ...
Cited by 8 - Related articles - BL Direct - All 5 versions

An object-oriented descriptive language to facilitate advanced handwritten form processing

C Cracknell, AC Downton, L Du - Image and Vision Computing, 1998 - Elsevier
... Other stages will also benefit from the encoding of extra information within a form **description
file**. ... The Form **Description Language** (FDL) proposed by Tang et al. [10] is primarily concerned
with allowing the **layout** of a form design's field objects to be recorded. Liu et al. ...
Cited by 4 - Related articles - All 5 versions

[PDF] MetaCore: An application-specific programmable DSP development system

JH Yang, BW Kim, SJ Nam, YS Kwon, DH Lee, ... - IEEE Transactions on ..., 2000 - Citeseer

... of transforming the given processor specification to lower level design, eg, chip **layout**. ... The processor **description language** is used to describe the target processor's instruction set and the ... The control files, ie, machine **description file** (MDF) and target description macros (TDM's ...

SALOGS-IV-A program to perform logic simulation and fault diagnosis

GR Case, JD Stauffer - Proceedings of the 15th Design Automation ..., 1978 - portal.acm.org

... provides for a direct preparation of input for the Sandia mask **layout** code ... The SALOGS Network **Description Language** (NDL) provides a natural way of describing circuit elements and ... The network **description file** (card deck or images) contains two parts: the model deck which is ...

From desktop to phonetop: a UI for web interaction on very small devices

J Trevor, DM Hilbert, BN Schilit, TK ... - Proceedings of the 14th ..., 2001 - portal.acm.org

... Navigation links are identified using a number of page **layout** heuristics. ... </**description**> </**language**> <icon output-format="text/x-hdml"> envelopel </icon> </presentation> </service> </service-group> ... is as simple as submitting a URL with the location of the service **description file**. ...

[PDF] Description mechanisms supporting the configuration and management of TTP/A fieldbus systems

S Pitzek - Master's thesis, Technische Universitat Wien, Institut ..., 2002 - vmars.tuwien.ac.at

... 36 iii Page 5. 3.1.3 Standard Generalized Markup Language (SGML) .... 36 3.1.4 Extensible Markup Language (XML) .... 37 3.1.5 Device **Description Language** (DDL) .... 39 3.2 Configuration and Management Frameworks ..... 40 ...

Method and system for maintaining hierarchy throughout the integrated circuit design process

C Carruthers, I Buchanan - US Patent 6,370,677, 2002 - Google Patents

... Similarly, Hardware **Description Language** (HDL) tools incorporate hierarchy in a manner similar to a software ... Thus, specific nodes may have a special custom **layout** to satisfy a particular ... present invention is the ability to access multiple physical cell **layouts** without disturbing ...

Mapping physical formats to logical models to extract data and metadata: The defuddle parsing engine

T Talbott, K Schuchardt, E Stephan, J Myers - ... and Annotation of Data, 2006 - Springer

... work is based on the draft specification for the Data Format **Description Language** and our ... parser supports the description of the content, structure and physical **layout** (endian-ness ... Applications can utilize the ESML library to parse this **description file** and transparently decode ...

[CITATION] High-level language extensions for run-time reconfigurable systems

TK Lee, A Derbyshire, W Luk, PYK Cheung - 2003 IEEE International Conference on ..., 2003

METHOD AND APPARATUS FOR AUTOMATICALLY COMMUNICATING RETURNING STATUS AND INFORMATION FROM A PRINTER USING ELECTRONIC MAIL (...

EN Chapman, MP Tompkins - US Patent App. 09/181,104, 1998 - Google Patents

... so that they can be readily decomposed or interpreted to define the detailed **layout** of the ... RIP) receives files from the print server 30 and functions as a page **description language** decomposer for ... If the driver doesn't support it (step 43) a page **description file** is created without the ...

Automatic calibration for output devices based on device output mode

H Van Hoof, P Adriaensen - US Patent 6,067,406, 2000 - Google Patents

... This electronic page **layout** is usually expressed in a page **description language** such as POSTSCRIPT (trademark of Adobe Systems Incorporated). AGFASCRIPT (trademark of Agfa-Gevaert AG, 30 Leverkusen in Germany) etc. ...

[CITATION] Arden-architecture development environment   ntu.edu.tw [PDF]

F Lai, F Tsaur, RJ Shang - ... , Communications and Automation towards the 21st ..., 1992

Method for linking a hardware description to an IC **layout**

DM Grant, JP Gray - US Patent 5,971,595, 1999 - Google Patents

... system for linking a hardware description to an for each bit size of a particular type of component, eg, integrated circuit **layout**. ... method utilizes parameterized to create a custom piece, eg, combining a single-bit adder 50 attributes in a hardware **description language**, a language ...

From coarse-grained components to DVE applications: a service-and component-based framework

X Zhang, D Gra•anin - ... of the twelfth international conference on 3D ..., 2007 - portal.acm.org

... map their design to the implementation, the framework provides a high level **description language** for specify ... The format of the behaviors **description file** is described in Section 4.3. ... describes the construction principles, communi- cation rules, interaction logic and the **layout** of the ...

Lessons Learned in Deploying a Wireless, Intranet Application on Mobile Devices

B Yoshimi, N Sukaviriya, H Derby, B Carmeli, B Bolam, ... - 2002 - computer.org

... environment: - The use of a high-level **description language** to describe screen **layouts**. ... fully specifying XML screens and constructing new components to accomplish screen **layout** tasks. ... allow the developer to understand the application from the kiosk **description file** alone. ...

[PDF] Report on the INCITS/V2 AIAP-URC standard   psu.edu [PDF]

J Nichols, BA Myers - 2004 - Citeseer

... appliance description to be easily internationalized by picking a resource **description file** for the ... common device (discussed further in the "3. No Specific **Layout** Information" section ... For example, UPnP supports a **description language** that has state variables and parameterized ...
Cited by 5 - Related articles - View as HTML - All 5 versions

[CITATION] Modeling distributed embedded applications on an interface file system
W Elmenreich, S Pitzek, M Schlager - ... Symposium on Object-Oriented Real-Time ..., 2004
Cited by 5 - Related articles - All 6 versions

psu.edu [PDF]

Imposition in a raster image processor
CH Benson, JT Green, M Parker - US Patent 6,411,396, 2002 - Google Patents
... 16 is configured to 40 receive page **description language** input data files and **layout** information from ... A page **description language** file sent to an imagesetter specifies objects to be displayed and 55 ... the availability of a job ticket and the receipt of the page **description file** page(s ...
Cited by 4 - Related articles - All 2 versions

Ad-hoc usage of web services with dynvoker
J Spillner, M Feldmann, I Braun, T Springer, A ... - Towards a Service-Based ..., 2008 - Springer
... the goal would be expressed as a direct link to the service **description file** as shown ... Among them are the creation of form elements, **layout** and embedding the form into ... However, alternative for-mats like WADL, the Web Application **Description Language**, exist for generic REST ...
Cited by 9 - Related articles - All 10 versions

selfip.net [PDF]

[PDF] NetSolve version 1.2: Design and implementation
H Casanova, J Dongarra - UT Department of Computer Science ..., 1998 - Citeseer
... Java applet is an attempt to provide a higher level tool to generate **description file**. ... by a language that does not provide low-level primitives to access the memory **layout**. ... team has been initiated in order to make concerted decisions about the **description language** that should be ...
Cited by 6 - Related articles - View as HTML - All 16 versions

psu.edu [PDF]

A MODEL-based object-oriented topology specification for network management
JY Chen, DL Yang, AC Liu - 2006 - dspace.lib.fcu.edu.tw
... ROC IP discovery and **layout** processes ... a better way for users to configure their network topology, and automatically generate a corresponding MODEL **description file**. ... [12] ITU-T Recommendation Z.100, "CCITT Specification and **Description Language** (SDL)," Recommendation ...
Cited by 5 - Related articles - All 5 versions

fcu.edu.tw [PDF]

[CITATION] Behavioral IP specification and integration framework for high-level design reuse
S Pillement, D Chillet, O Sentieys - ... Symposium on Quality Electronic Design, 2002. ..., 2002
Cited by 6 - Related articles - All 4 versions

Toolipse: An IDE for development of JIAC applications
EO Tuguldur, A Heßler, B Hirsch, S Albayrak - Programming Multi-Agent ..., 2009 - Springer
... Framework (EMF), visualise them graphically with the Graphical Editing Framework (GEF) and

provide simple graphical **layouts** such as radial **layout**, zooming and **...** arbitrary JADL knowledge
files, the latter is more specific, in that it creates a service **description file** as well as **...**
Cited by 5 - Related articles - All 3 versions

[PDF] Simplified grid implementation of medical image processing algorithms using a workflow management system   unice.fr [PDF]

D Krefting, M Vossberg, T Tolxdorff - ... Computer Systems, in this issue (doi: ... - i3s.unice.fr
... and software is provided in the XML-based D-Grid Resource **Description Language** (D-GRDL **...**
is particularily helpful in the development and testing phase, as workflow **layout** and
implementation **...** To publish the new software to the grid, a resource **description file** is generated **...**
Cited by 7 - Related articles - View as HTML - All 4 versions

KBSIM: a system for interactive knowledge-based simulation

M Hakman, T Groth - Computer methods and programs in biomedicine, 1991 - Elsevier
... complicated programming for Unix shared memory, queues and semaphores, etc., a simple table
layout was communicated **...** 2.4.1. Rule **Description Language** The Rule **Description Language**
is based on the production rule model. **...** 9. The rule **description file** has three sections. **...**
Cited by 8 - Related articles - All 4 versions

Scripting language for distributed database programming

DTS Wu, JG Ichnowski - US Patent 6,243,711, 2001 - Google Patents
... Freivald et al 709/2 Hill et al 707/513 cited by examiner Primary Examiner—Wayne Amsbury
Assistant Examiner—Thu-Thao Havan (74) Attorney, Agent, or Firm—Blakely, Sokoloff, Taylor
& Zafman LLP (57) ABSTRACT A system for providing a form **description language** for a **...**
Cited by 4 - Related articles

Recent Advances in the CLiDE Project: Logical **Layout** Analysis of Chemical Documents

A Simon, AP Johnson - J. Chem. Inf. Comput. Sci, 1997 - ACS Publications
... They are important for differentiating between **layouts** occurring under the described page
conditions, ie, to **...** identification is one of the many tasks accomplished by the logical page **layout**
detection algorithm. The process starts by interpreting the given document **description file**. **...**
Cited by 2 - Related articles - BL Direct - All 4 versions

Algorithm and methodology for the polygonalization of sparse circuit schematics

DJ Schmidt, P Chun, RC Saito, EY Chen - US Patent 6,904,571, 2005 - Google Patents
... the positional relationship of the interconnections in the schematic is maintained in the **lay- out**. **...**
6,904,571 BI Also provided are methods of automatically generating a custom element **layout**. **...**
a first method, a number of transistor types are described in a template **description file**. **...**
Cited by 4 - Related articles - All 2 versions

[PDF] The **description language** chosen for FDNTEX   tug.org [PDF]

B Bayart - tug.org
... The **description language** chosen for FDNTEX **...** It is easy to state these dependencies in the
description file, even if the information is hard to obtain. **...** Imagine a document class that has an
option psfonts that can be called to adapt the **layout** to PostScript fonts instead of traditional **...**

[CITATION] An MPEG-4 tool for composing 3D scenes

P Daras, I Kompatsiaris, T Raptis, MG Strintzis - IEEE Multimedia, 2004
Cited by 11 - Related articles - BL Direct - All 10 versions

[PDF] COBOL/SRE: A COBOL System Renovation Environment

A Engberts, W Kozaczynski, E Liongosari, JQ ... - Proc. of the 6th Intl. ..., 1993 - Citeseer
... The user may create the system source file objects one by one, by interacting with COBOL/SRE's
dialog windows. Alternatively, the user may provide a system **description file** to load and link the
files into COBOL/SRE automatically in one pass. ... Data **layout** analysis. ...
Cited by 5 - Related articles - View as HTML - All 35 versions

Automatic hypertext conversion of paper document collections

A Myka, U Güntzer - Digital Libraries Current Issues, 1995 - Springer
... The developed link type **description language** LITHP (Link Type **description language** for
HyperText ... as detected during preprocessing, are taken from a structure element **description file**
that can be ... Within paper documents however, an author may have used **layout** properties in ...
Cited by 11 - Related articles - BL Direct - All 10 versions

Process for the creation of camera-ready mechanicals

JF Geraci - US Patent 5,029,115, 1991 - Google Patents
... Map Color files 6.1.1 Read next line of data from the Page **Description file** t RETURN ... were
designed to function in a color environment while 10 page **layout** software was ... The page ated
by an object-oriented vector-based illustration **description language** translates the level of ...
Cited by 10 - Related articles

[PDF] ASIP architecture for future wireless systems: flexibility and customization

JR Cavallaro, P Radosavljevic - Wireless World Research Forum (WWRF), 2004 - Citeseer
... co-design flow (from HLL code of channel equalization to processor **layout**) is presented on ... a
set of software tools that performs transformation from one hardware **description language** to
another. Starting point of the design flow is machine **description file** of the optimal target ...
Cited by 4 - Related articles - View as HTML - All 14 versions

Tools for remote computing in accelerator control

PS Anderssen, V Frammery Rainer - Nuclear Instruments and Methods in ..., 1990 - Elsevier
... To write, the Interface **Description File**, a special **description language** is used. it is based
on the IvIESA language from Xerox [13] and its grammar is fully specified in ref. [111, using
the PachusNaur format. The **layout** of the file is shown in fig. ...
Cited by 3 - Related articles - All 4 versions

[CITATION] A template-based MHP authoring tool

HT Chiao, KS Hsu, YK Chen, SM Yuan - The Sixth IEEE International Conference on ..., 2006
Cited by 3 - Related articles - All 9 versions

Smartphone views: building multi-device distributed user interfaces

M Sjölund, A Larsson, E Berglund - Mobile Human-Computer Interaction– ..., 2004 - Springer

... a platform for GUI distributions requires a general-purpose way of describing GUI **layout**, without the ... The choice of Views as GUI **description language** was based on several con- siderations. ... The menu can be implemented in the GUI **description file** with the rest of the GUI, with ...

Cited by 3 - Related articles - BL Direct - All 4 versions

Development of a timing analysis program for multiple clocked network

E Chan - Proceedings of the 22nd ACM/IEEE Design Automation ..., 1985 - portal.acm.org

... simulated at a detailed level, 2) long learning time for the network **description language** and the ... It reads in a network **description file** and a control file and output any violation ... K Ogawa, T Nakano "Path Delay Analysis for Hierarchical Building Block **Layout** System" Proceedings ...

Cited by 3 - Related articles - All 3 versions

[PDF] A Framework for GUI-driven Design Space Exploration of a MIPS4K-like processor

S Pasricha, P Biswas, P Mishra, A Shrivastava, ... - Center for Embedded ..., 2003 - Citeseer

... EXPRESSION [2] [5] is an ADL (Architecture **Description Language**) that can capture a processor-memory architecture ... Figure 2: VSAT-GUI **layout** for the acesMIPS architecture ... internally in appropriate data structures and then used to create the textual ADL **description file** of the ...

Cited by 4 - Related articles - View as HTML - All 20 versions

File arrangement and maintenance executive (FAME). A general purpose schema driven file maintenance system

BW Brown, J McConathy, M Meigs, B Munger, ... - Computers in Biology ..., 1978 - Elsevier

... 4. Specification of the **layout** of the first card (Fig. 1) of the Header file for entry into FAME. A similar specification must be made for the second card (Fig. ... QED Information Sciences, Inc., Wellesby, MA (1975). 2. CODASYL Data **Description Language** Journal of Development. ...

Cited by 4 - Related articles - All 2 versions

Logic synthesizer

M Komoda - US Patent 5,438,524, 1995 - Google Patents

... large wiring load is charged as well as adverse effects described in HDL (Hardware **Description Language**). ... worst case, there arises the problem that description received from the function **description file** 5 wirings ... do into a logic such as AND, OR, etc., and it further com- **layout**. ...

Cited by 6 - Related articles

The WorldGrid transatlantic testbed: a successful example of Grid interoperability across EU and US domains

F Donno, V Ciaschini, D Rebatto, L Vaccarossa, ... - Arxiv preprint cs/ ..., 2003 - arxiv.org

... Wherever possible a PACMAN **description file** used, as source, the correspondent package RPM ... PDA's, cell phones...); they present the same user interface to several back-ends, preventing the user from learning different Grid "dialects"; they have user-friendly **layout** and make ...

Cited by 12 - Related articles - View as HTML - All 18 versions

An XForms based solution for adaptable documents editing

I Beszteri, P Vuorimaa - Proceedings of the 2005 ACM symposium ..., 2005 - portal.acm.org
... The document **description file** is identified by a unique name, given by the user. ... The **layout** description model of AHA! is similar to XFrames. ... Since the adaptation works with the XFrames information only, the solution does not depend on the document **description language** itself. ...
Cited by 2 - Related articles - All 4 versions

[PDF] Usable firewall configuration   psu.edu [PDF]
W Geng, S Flinn, J DeDourek - Proc. of the 3rd Ann. Conf. on Privacy, ..., 2005 - Citeseer
... We utilized this package to implement the **description language** parser so that the **description file** conforms with the JavaScript syntax. • JGraph: JGraph is an open source graph draw- ing and **layout** component that is purely written in Java (www.jgraph.org). ...
Cited by 4 - Related articles - View as HTML - All 9 versions

The ganga user interface for physics analysis and distributed resources   cern.ch [PDF]
A Soroko, CL Tan, D Adams, K Harrison, P ... - Computing in High ... - cdsweb.cern.ch
... AJDL is the Abstract Job **Description Language** used in the ADA system. ... There is a multi-purpose Figure 1: Screenshot showing the general **layout** of the Ganga GUI. ... The job handler in turn generates the JDL file and XML job-**description file** required by DIRAC. ...
Cited by 3 - Related articles - View as HTML - All 2 versions

Photocollage generation and modification
SL Shaffer, JR Squilla, JK McBride - US Patent App. 09/222,393, 1998 - Google Patents
... into digital storage the customerdecides on type of product 151 153 Customer returns modified first cut photocollage, cover and **layout** pages 155 Modified first cut photocollage, cover and **layout** pages scanned to determine images to be used, cover image, **layouts** and product ...
Cited by 7 - Related articles - All 2 versions

Automation systems for generating web-based mathematical learning and presentation materials
Y Shirota - International Journal of Computational Science and ..., 2006 - Inderscience
... **layout** representations are written in HTML in which the logical structures and physical **layouts** are not ... the maximum profit of a firm (Figure 5). In the meta-level **description file** shown in ... is that they produce web pages of uniform appearance whose content, **layout**, and generated ...
Cited by 3 - Related articles - BL Direct - All 6 versions

[PDF] An information model for finding and integrating distributed resources for engineering design-manufacturing processes   yonsei.ac.kr [PDF]
R Kulkarni, D Rosen, JK Allen, F ... - Proceedings of the ..., 2002 - cadcam.yonsei.ac.kr
... The web service **description language** (WSDL) [6] is an XML based format accepted internationally for ... Figure 4: Information Model **Layout** for Design and Manufacturing ... and corresponding living systems theory function keywords are stored in the resource **description file**. ...
Cited by 5 - Related articles - View as HTML - All 2 versions

[PDF] Substation Automation based on IEC 61850   abb.com [PDF]
I De Mesmaeker, P Rietmann, KP Brand, P ... - 6th Regional CIGRÉ ..., 2005 - abb.com
... The use of the already mentioned standardized Substation Configuration **description Language**

(SCL) has an ... Also the geographical **layout** (extension, buildings, cable channels, etc) may have an impact ... documented also with a SCL file called IED Capability **Description file** (ICD ...

Cited by 3 - Related articles - View as HTML - All 3 versions

Apparatus and method of modifying hardware **description language** statements

K Kucukcakar, R Gupta, T Tkacik - US Patent 5,533,179, 1996 - Google Patents

... Robert D. Atkins [57] ABSTRACT An Hardware **Description Language** (HDL) **description file** (12) is ... the computer generated implementation and creates gate-level schematics, **layouts**, and masks ... object such as a register file can effect placement, **layout**, routing, arrangement of ...

Cited by 1 - Related articles

[CITATION] A case study in process independence

N Royal, J Hunter, I Buchanan, LL Ltd, S Edinburgh - 22nd Conference on Design ..., 1985

Cited by 2 - Related articles - All 3 versions

Microcode development for microprogrammed processors

J Hwang, CA Papachristou, DD ... - Proceedings of the 18th ..., 1985 - portal.acm.org

... application to the VLSI chip environment. Essentially, a **description language** at this level can express logic design equations in a form that can be efficiently compiled into 3 **layout** diagram [17]. Most notable examples of such **layout** ...

Cited by 3 - Related articles - All 3 versions

Parallel redundant interpretation in a raster image processor

MR Hughes - US Patent 6,295,134, 2001 - Google Patents

... printing device, the sheet including one or more pages of content defined a page **description file**, the method including receiving a page **description file** defining one ... Digital press 16 is configured to receive page **description language** input data files and **layout** infor- mation ...

Cited by 2 - Related articles

Design for test area optimization algorithm

L Souef, J Bombal, B Ginetti - US Patent 6,311,318, 2001 - Google Patents

... SOFTWARE BEHAVIOR **DESCRIPTION FILE** LOGIC DESIGN SYNTHESIS PROGRAM (VHDL) DETAILED **DESCRIPTION FILE** 42 59 ... computer aided design (CAD) tools are used to realize a circuit **layout**. The use of :5 CAD tools to **lay out** complex integrated circuits, such as ...

Cited by 2 - Related articles

Database forms with attached audit history

JWC Kao, M Hill, JG Ichnowski, DTS Wu - US Patent 6,070,177, 2000 - Google Patents

Page 1. United States Patent Kao et al. US006070177A [ii] Patent Number: [45] Date of Patent: 6,070,177 May 30,2000 [54] DATABASE FORMS WITH ATTACHED AUDIT HISTORY [75] Inventors: James Wei-Ching Kao, Fremont ...

Cited by 7 - Related articles

Model-driven architecture for mobile applications

J Dunkel, R Bruns - Business Information Systems, 2007 - Springer
... The mobile user interface can be char- acterized by two different aspects: (a) The **layout** of each
screen on the mobile device. ... With XForms a W3C standard has been chosen as the mobile user
interface **description language**. ... 12. Part of the generated workflow **description file** ...
Cited by 2 - Related articles - BL Direct - All 4 versions

Automatic generation of programmable logic device architectures
V Betz, J Rose - US Patent 7,051,313, 2006 - Google Patents
... 5 is an example architecture **description file**; allow PLD designers to write software code (in either
C ... FIG 12 is a simplified block diagram of a tiled **layout** Accordingly, there is a ... performance
evaluation of a wide With a high-level PLD architecture **description language**, variety of ...
Cited by 3 - Related articles - All 2 versions

FACE: Fine-tuned Architecture Codesign Environment for ASIP Development   ntu.edu.tw [PDF]
IH Jeng, F Lai, YD Tseng - Design Automation for Embedded Systems, 1999 - Springer
... user and machine **description file** contains constant definitions of register, storage, and stack
layout. ... Ar- chitecture **description file** shares information in-between to make partition ... source code,
where as in others software parts are translated into a hardware **description language**. ...
Cited by 2 - Related articles - All 3 versions

SAM-Grid: A system utilizing grid middleware and SAM to enable full function grid computing   fnal.gov [PS]
A Baranovski, G Garzoglio, L Lueking, DSI ... - Nuclear Physics B- ..., 2003 - Elsevier
... for "Sequential Access to data via Metadata" where the "sequential" refers to the **layout** of physics ...
This job **description file** (JDF) is processed by the client (user interface in EDG terminology) so
as to ... We will build a Job **Description Language**, and in doing so we will desire a new ...
Cited by 13 - Related articles - All 5 versions

An integrated approach to rapid product development for embedded automotive control systems
U Lefarth, U Baum, T Beck, K Werther, T ... - Control Engineering ..., 1998 - Elsevier
... performed by separate teams: 1. Function development: control-system **layout**, some- times ... ECU
with integ- rated ERCOS operating system, but also an ASAP2 **description file**. ... editor, the model
can be specified textually in ESDL (Embedded Software **Description Language**). ...
Cited by 3 - Related articles - BL Direct - All 2 versions

A rapid prototyping system for distributed information system applications
W Zhou - Journal of Systems and Software, 1994 - Elsevier
... PSDL (Luqi and Ketabchi, 1988; Luqi and Lee, 1989) is a prototypesystem **description language**. ...
The main purpose here is to gener ate RPC functions for servers that perform data baseoriented
operations and to generate some screen **layout** modules for client programs. ...
Cited by 3 - Related articles - All 4 versions

[CITATION] Creating XML Based Scalable Multimodal Interfaces for Mobile Devices   bme.hu [PDF]
B Tóth, G Németh - Mobile and Wireless Communications Summit, 2007. ..., 2007
Cited by 2 - Related articles - All 2 versions

[CITATION] The system-independent data exchange format CAEX for supporting an automatic configuration of a production monitoring and control system
M Schleipen, R Drath, O Sauer - ... Symposium on Industrial Electronics, 2008. ISIE ..., 2008
Cited by 5 - Related articles - All 3 versions

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